IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventor(s):

Chyan

Case:

15-6-9

Serial No.:

09/648164

Filing Date:

8/25/2000

Examiner:

Dickey

Group Art Unit:

2826

United States Patent Office on November 5. number 703-872-9318 addressed to this Cor

Title:

ARCHITECTURE FOR CIRCUIT CONNECTION OF A VERTICAL

TRANSISTOR

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D. C. 20231

SIR:

<u>PETITION FOR EXTENSION OF TIME</u>

Applicant(s) petition(s) the Commissioner of Patents and Trademarks to extend the time for response to the action dated 5/05/04 for <u>3 (Three) months</u> from <u>8/05/04</u> to <u>11/05/04</u>.

11/16/2004 AJOHNSTEQUITED TO COMPETE the SERVER Duplicate copies of this petition are enclosed.

01 FC:1253

980.00 DA

Respectfully Submitted,

Ву:__

Ferdinand M. Romano

Reg. 32,752

Attorney for Applicant

Date: 5 November 2004

PAGE 29 * RCVD AT 11/5/2004 2:05:38 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/0 * DNIS:8729318 * CSID: * DURATION (mm-ss):02-14

	PATENTA	Effective December 29, 1999									
CLAIMS AS FILED - PART I (Column 1) (Column 2)							LL E	ENTITY	OR	OTHER SMALL	
F	OR	NUM	BER FILED	NUMBER	EXTRA .	RAT	Έ	FEE	1	RATE	FEE
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* If the difference in column 1 is less than zero, enter "0" in column 2							AL		OR OR	TOTAL	368 (1)
CLAIMS AS AMENDED - PART II							,	<u></u>	Jon	OTHER	
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FORM PTO-875 (Rev. 12/99)

Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

Application or Docket Number